A REVIEW ON LASER ASSISTED DEPOSITION AND MICRO-ETCHING OF SILICON CARBIDE (SiC) COATING FOR MEMS DEVICE APPLICATIONS

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Abstract

Single crystal silicon carbide (SiC) material is highly suitable for the development of Micro Electro Mechanical Systems (MEMS) devices.SiC are highly suitable for high-temperature application due to its mechanical robustness, chemical inertness and electrical stability at elevated temperatures. Wide varieties of reports are published on the convention Chemical Vapour Deposition (CVD) and chemical etching techniques based Micro-Fabrication and micro etching of SiC in particular for MEMS device. But however laser based SiC micro fabrication and micro-etching have their own advantages as compared to the conventional system. This paper highly focuses on how well the laser based micro fabrication and micro —etching can be tailored for the development of SiC based MEMS device.

Key words: MEMS, Laser based coating, micro-etching, sensors for high temperature applications.

I. INTRODUCTION

Over the past decade, Micro-Electro Mechanical Systems (MEMS) technology has evolved from a niche technology into an important mainstream technology for a wide range of applications including micro-sensors and actuators(1-3). MEMS can sense their environment and have capability to react to changes in the environment. Among the various materials used for such applications, Silicon has been the preferred choice. But the high demand of sensors capable of operating at a temperature well above 300°C, often in severe environments, motivated the search for alternative materials. Silicon Carbide (SiC) possesses excellent material properties, wider band gap, higher breakdown voltage, much higher heat conductivity and melting temperature than Silicon. This makes SiC a promising material for high temperature (>600°C) MEMS sensors (pressure, temperature, gas and optical) and actuators. Among all MEMS based devices, the demand of SiC based pressure sensors growing in numerous industries including transportation (automobile, diesel and aerospace), energy (power generation, petrochemical refineries and oil & gas industries), and environmental (HVAC systems). SiC exists in a number of different forms, but only three polytypes (3C-SiC, 4H-SiC, and 6H-SiC) could be grown in single crystalline conforming suitability for microelectronics and MEMS industries(4-6). Therefore, the performance and reliability of SiC MEMS devices depend on the

MEMS fabrication techniques(7-8). The based fabrication techniques basically include the material deposition and material etching. A variety of techniques are used to grow single and polycrystalline forms of SiC thin films, including atmospheric pressure CVD (APCVD), sputtering, metal-organic CVD, atomic layer deposition (ALD) etc. Though CVD is a relatively old and powerful method for producing crystalline SiC(9-12), it has limitations as it needs high processing temperature and the possibility of contamination is quite high. In the case of micro etching, conventional fabrication methods, such as reactive ion etching (RIE), electron cyclotron resonance (ECR), deep reactive ion etching (DRIE) do not reach the goal easily due to high hardness and chemical stability of SiC (13-16). Moreover, these methods have limitations due to poor etch selectivity and slow etching rate preventing large volume production of SiC MEMS devices at low cost(17-20). These limitations can be overcome using laser micro-fabrication and micro-etching techniques.

Laser micro-fabrication and micro-etching has a great potential as a MEMS fabrication technique owing to its material flexibility and 3D capabilities. Due to precise control on laser variables, such as wavelength, pulse width, repetition rate, laser fluence (energy per unit area) etc., high-quality microstructures can be fabricated easily in shorter time(21,22). This review paper presents a brief overview on the functionality of SiC as a MEMS device and requirements for different micro fabrication and

micro-etching techniques. The limitations of conventional micro-fabrication and micro etching techniques and how these limitations can be overcome by Laser assisted micro-fabrication on and micro etching of SiC for MEMS application is also discussed.

II. SIC FOR MEMS BASED STRUCTURES

Silicon Carbide, also known as carborandum, is a compound of silicon and carbon with a chemical formula of SiC. SiC has polytypes consisting of 50% carbon atoms covalently bonded with 50% silicon atoms having its own distinct set of electronic properties. Though more than 100 polytypes are known, only a few are commonly grown in a reproducible form and acceptable for semiconductor applications. The three most commonly used polytypes are 3C, 6H and 4H. The 3C polytypes, also known as beta-SiC (or β – SiC), exhibits cubic structure, crystallizes in a ZnS-type structure and grows epitaxial on silicon substrates. It requires much lower substrate temperature (1500°C or less) as compared to others during CVD. In contrast, 6H-SiC and 4H-SiC, both in hexagonal, alpha form, do not grow on other substrates except 6H-SiC and 4H-SiC which requires higher temperatures (up to 2000°C). All these three polytypes are relevant for MEMS applications. Their elastic compliance and constants can be found elsewhere: 3C-SiC(24-26), 4H-SiC(27-33) and 6H-SiC(34-38). The fracture toughness of MEMS-relevant thin films and structures was reported to be 3-4 times higher than for silicon (39). For the hardness, no reliable data are available. The reported values of Knoop hardness for 3C (40) and 6H-SiC(40-41) are 2480 $\mbox{kg}\mbox{ mm}^{-2}$ and 1830 kg mm⁻² respectively, while the micro-hardness for 6H-SiC is 22.9 GPa (42) and 25 GPa(43).The values for Young's modulus reported in the literature are in the range of 380 to 700 GPa for 3C-SiC. The anisotropy in cubic crystals is given by 2C44/(C11 -C12)(44) and the reported values yields an anisotropy factor between 1.6 and 2.2 (45-48) indicating Young's modulus is expected to be anisotropic. Consequently, Young's modulus for polycrystalline SiC films is lower than that for single crystalline SiC. These properties make the different structures of SiC for different MEMS application as listed in Table 1.

Table 1. Properties of SiC Structures

Different	Structure	Bandgap	Thermal	Density	Melting
SiC		(eV)	conductivity	(g/cm ²)	point
structures			(1/K)	, ,	(K)
3C-SiC	T ² _d -F43m	2.4	3.6	3.21	3103
4H-SiC	C ⁴ _{6v} -P6 ₃ mc	3.2	4.9	3.21	3103
6H-SiC	C ⁴ _{6v} -P6 ₃ mc	3.0	4.9	3.21	3103

A. Pressure Sensors

6H-SiC-based pressure sensor, can exhibit a full-scale output of 40.66 mV at 1000 psi and 25°C, decreasing to 20.33 mV at 500°C, is reported (49). The sensor was tested in the temperature range between 25-400°C and at pressures up to 500 kPa. The sensor showed a linear output voltage over the pressure range and a temperature coefficient of sensitivity of 0.16%/°C at 400°C.

B. Optoelectronics and Sensors

The wide bandgap of 6H-SiC is also useful for realizing low photodiode dark currents, as well as sensors that are blind to undesired near-IR wavelengths produced by heat and solar radiation. Commercial SiC-based UV flame sensors, based on epitaxially grown, dry-etch, mesa-isolated 6H-SiC p-n junction diodes, have successfully reduced harmful from gas-fired ground-based pollution emissions turbines used in electrical power generation systems(50).

C. Lateral Resonant Devices

Lateral resonant devices are basic to surface micromachining and are used extensively as building blocks for a variety of sensor and actuator implementations. Si based lateral resonant devices, however, are not suitable for operation in high temperature environments. The first reported SiC-based surface-micro machined lateral resonant devices were fabricated using poly-SiC films deposited on polysilicon sacrificial layers(51).

D. Temperature Sensors

A poly-SiC thin-film thermistor on an alumina substrate was first reported in 1990 (52). The sensor was fabricated from RF-sputter-deposited SiC, using sintered SiC as a target. The substrate was maintained at a deposition temperature of 650°C. The device was tested at temperatures from 0 to 500°C. The results

showed that the thermistor constant (B) increased linearly with temperature over the entire temperature range. Compared with conventional metal-oxide thermistors, the temperature coefficient of resistance (TCR) for the SiC devices decreased slowly with increasing temperature. Additionally, the sensor exhibited good thermal stability and a rapid thermal response with a resistance change of only 5% after a continuous test at 500°C for 1000 h (53-55).

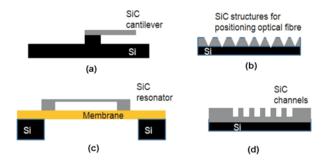


Fig. 1. Different structures of SiC based MEMS device

Figure 1 shows the different structures of SiC designed for MEMS device application. Developing these structures requires a high precise fabrication technique and micro etching technique.

III. MICRO FABRICATION AND MICRO-ETCHING OF SIC

A. Conventional Micro-fabrications techniques

Atmospheric pressure CVD (APCVD), sputtering, metal-organic CVD, atomic layer epitaxy, molecular beam epitaxy, are the current techniques used to grow single, amorphous and polycrystalline forms of SiC bulk and thin films(56-60). Conventional silicon bulk micromachining techniques can be used for fabricating single-crystal, poly and amorphous SiC with some difficulty. Single crystal and poly-SiC layers between a few microns and several tens have been used for BMM structures. Their mechanical properties can be adjusted during or after growth, so to obtained low stress structures.

Amorphous SiC films are generally thinner as stress is more difficult to control. Therefore, they are often used it in sandwich configurations together with silicon oxide, nitride or poly-silicon. Single-crystal SiC films are grown directly on single-crystal silicon. Therefore, no conventional surface micromachining is

possible with crystalline layers on silicon. Poly-crystalline and amorphous SiC layers, on the other hand, can be grown on various substrates and are suitable for surface micromachining. Poly-SiC is generally grown on a poly-Si layer or deposited on oxide layers. The SiC is the mechanical or structural layer while the poly-Si or the oxide is used as sacrificial layer. When poly-Si is used as sacrificial layer, KOH or TMAH are used to release the SiC microstructures so that the oxide can protect the underlying silicon during the final sacrificial etch. If the oxide is used as sacrificial layer, HF based solutions are used, just as in conventional silicon surface micromachining. No protection of the mechanical layer is required since SiC is HF resistant. Multi-layer structures are also possible. If an oxide layer is grown under the poly-Si layer, the oxide could be used as sacrificial layer and both poly-Si and poly-SiC can be used as mechanical layers (61-66). As SiC is grown on Si wafers and as plasma etching of both Si and SiC is possible in the same fluorine based chemistry, it is possible to apply DRIE to the SiC/Si system as well.

However in the above techniques, the chemical stability of the SiC makes it difficult to perform micro-fabrication process such as etching. Reactive ion etching, photochemical etching, inductively coupled plasma and electron cyclotron resonance methods are used to etch, but at very low rates. For example, a maximum etch rate of 0.97 %m/min was achieved of SiC to Si and SiO₂ is poor. Micro-fabrication of MEMS device or via holes for high frequency electronics devices require deep etching (up to 400%m) and, consequently, high etch rate processes are desired. These limitations can be completely overcome using pulsed laser micro-fabrication techniques to improve the etch rate as well as feature quality in applications like micro motors, resonators, micro gripper etc, the demands of industrial manufacturing for fast, precise Micro-Fabrication are better met by the robust, reliable, high repetition rate and high power Nano/Femto and Pico- second lasers(67-69).

B. Laser based micro-fabrication (SiC Deposition)

Pulsed laser deposition (PLD) has found wide attention over the past decade for its ease of use and success in depositing thin films of complex stoichiometry materials. Figure 2 presents the schematic layout of PLD deposition of SiC on silicon substrate. In laser ablation, high-energy laser pulses

are used to evaporate the matter from the surface of SiC target in such a way that the stoichiometry of the material is faithfully preserved during the interaction. A supersonic jet of particles (plume) is ejected normal to the target surface. The plume, similar to the rocket exhaust, expands away from the target with a strong forward-directed velocity distribution of the different particles. The species condense on the substrate placed opposite to the target. The ablation process takes place in a vacuum chamber - either in vacuum or in the presence of some background gas, depending upon the applications. The major parameters involved in this process are (70-74)

- Type of laser, pulse width (nanosecond, picosecond and femtosecond)
- 2. Laser fluence (i.e. the energy per unit of area) and wavelength
- Structural and chemical composition of the target material
- 4. Chamber pressure and the chemical composition of the buffer gas

5. Substrate temperature and the distance between the target and the substrate.

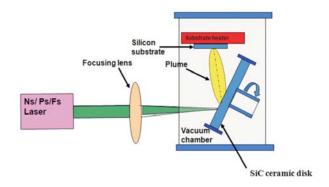


Fig. 2. Schematic sketch of Pulsed Laser Deposition.

The laser ablation mechanisms involve many complex physical phenomena such as collision, thermal, and electronic excitation, exfoliation and hydrodynamics. The phenomenon's are highly governed by the pulse width of the laser used. Ablation of targets with materials with nanosecond/femtosecond lasers has their own influence in the deposition of the SiC. These PLD techniques can be appropriately tailored for the

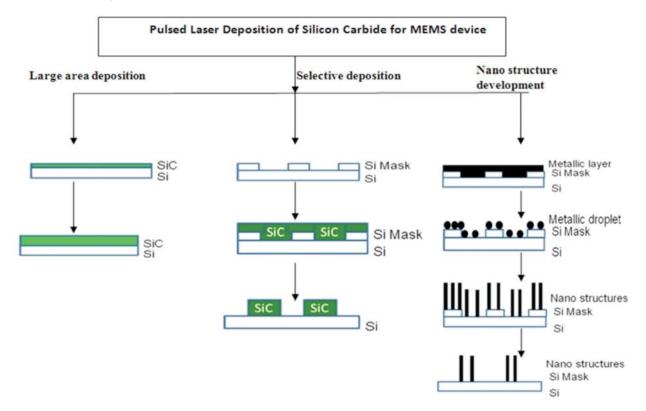


Fig. 3. Classification on PLD based micro-fabrication for development of SiC based devices.

growth process of SiC for different MEMS device development. PLD of SiC for MEMS applications are classified in to three based on the MEMS device development requirement. 1) Large are thin film deposition 2) selective deposition 3) positioning of nanostructures as shown in Figure 3.

In PLD technique, large area deposition is performed by moving the substrate or by rotating the target with a particular angle. CVD has its own limitations on selective area deposition, which can be overcome with PLD. With nanosecond laser 3C-SiC was deposited on Silicon (100) substrates using PLD. The deposition was performed at different temperature conditions ranging from 550 to 700°C. Thin SiC films were grown on (100) Si and Silicon (111) substrates. The different parametric condition including the laser repetition rate, and laser fluence were optimised for efficient deposition of SiC (75,76). Amorphous SiC and SiCxNv films have been deposited by pulsed laser deposition on single crystal silicon substrates by KrF (248 nm) excimer laser ablation of a SiC sintered target in a vacuum system at room temperature using nonreactive, Ar, and reactive, N2, background gases at different pressure (77). The particulate density obtained in the present case (fs-PLD) was much lower than those generated by nanosecond-pulsed excimer laser ablation of SiC (78). Where it was concluded that films deposited by the femtosecond pulsed lasers were superior to those deposited by the nanosecond pulsed lasers. With long pulses such as nanoseconds. explosive ejection of liquid droplets from the target occurs because of superheating of the subsurface layers, solid-liquid phase change, thermal expansion of the material, and surface degradation. In fs-PLD, the pulses greatly modify the thermal processes in ablation because of three reasons: the coupling of a large optical field with the solid target, electron thermalization and electron-phonon coupling.

SiC, Si and alkali free glass can act as a good masking agent where the appropriate are which need to be deposited are positioned as shown in figure 3. Formation of silicon carbide ridge, line and block, basically depends on the size and shape of the mask. In addition, there are also reports on selective are laser deposition by moving the laser beam to deposit solid material from precursor gas (79). In this study, tertamrthylene silane (Si (CH₃)₄) as a precursor to deposit silicon carbide. These deposition rates are

much higher than those that could be obtained with the 248 nm KF excimer laser depositions (with a laser fluence of 2.5 J/cm² at room temperature) (80) and are comparable to those obtained by the CVD method. Laser sintering is accompanied by a reaction between sintering powders or sintering powder(s) and a gas precursor. The sintering/reaction combination results in a solid layer of material compositionally different from the powder source(s). Successive layers of powder are spread and selectively laser reaction sintered to build up the desired shape (81). Selective Area Laser Deposition (SALD) Joining was utilized in fabricating ioined silicon carbide structures. Specifically, silicon carbide tubes were 'welded' together by depositing silicon carbide from a gas phase reaction. A single laser beam deposition setup and a dual laser beam design were investigated. A gas environment of tetramethyl silane and hydrogen served as the deposition precursors (82). Selective area laser deposition vapour infiltration (SALDVI) techniques are also developed to selectively deposit SiC (83)

Nanostructured silicon carbide (SiC) has shown to exhibit superior properties (greater elasticity and strength) as compared to that of bulk SiC and has potential applications in light-emitting diodes and UV photo-detectors due to higher light-emission efficiency (84,85). There is significant interest in the synthesis of nanostructure SiC including nano-spheres, nanowires, nanorods and so on as novel functional materials for nano-scale engineering. Recently reported the chemical vapour deposition (CVD) synthesis of SiC nano-cones on the surface of silicon oxy carbide and synthesized SiC nanorods via catalyst-assisted crystallization of amorphous silicon carbon nitride. Ho et al.(84) used a (VLS) vapor-liquid-solid process nano-flower-like structures with the help of gallium nitride powder. Dai et al.(85) reported the synthesis of SiC nanowires by using a carbon template method. However, these products were available just based on complicate fabrication process, cost of expensive carbon nano-tubes or the use of the explosive precursor and so on. In addition, the synthesized materials were low yield and time consuming. PLD was also deployed to synthesize random oriented SiC nanostructures without using any catalyst. However, positioning of SiC nanostructures is highly in demand. A catalyst based nanostructure growth using PLD for ZnO has been reported (86-90).

C. SiC Bulk Micromachining

The bulk micro-machining is a conventional technique, in which the selective removal of the substrate material in order to realize the miniaturized components. Bulk micromachining using chemical means are widely used in MEMS industry. The chemical etching basically involves the immersion of a substrate into a solution of reactive chemicals that will etch exposed region at the measurable rates. This chemical based etching technique can be used for bulk micro machining of single-crystal, poly and amorphous SiC (91). In the case of single-crystal generally epi-SiC on Si is used, although a BMM SiC pressure sensors using SiC on bulk substrate has been reported(92). Electrochemical etching from the backside has been used to realize 25 mm-thick membranes containing piezo-resistors (93-94).However, current micro-fabrication methods for SiC are quite limited when compared against Si. Because anisotropic wet etchants do not work well, patterning techniques based on photolithography reactive ion etching (RIE) is used for SiC. RIE etching with SF₆/O₂ plasma, SF₆/O₂ inductively coupled plasma (ICP) and deep reactive ion etching (DRIE) processes were traditional methods to etch SiC thin films, however, at very slow rates.

Photoand electro-chemical wet etching processes are alternatives to RIE and DRIE process. In these alternatives, SiC is first anodized forming a deep porous layer and subsequently removed through thermal oxidation followed by dipping in HF. These processes suffer from poor selectivity to polysilicon and silicon dioxide sacrificial layers, low etch rates and complex steps (95,96). Another approach to patterning SiC microstructures involves the use of micro-molding, whereby an inverse pattern is etched into a sacrificial layer such as silicon dioxide. SiC is deposited into the conformal mold and then planarized using true post-processing, chemical-mechanical polishing. Another major challenge is the selective etching of SiC films or bulk materials as SiC is not etched significantly by most of acids and bases at temperature less than 600°C. This makes wet etching of SiC more difficult to accomplish and directly affects the crystallographic planes of the substrates. However Laser based micro machining can be an alternative approach (97). Due to higher chemical stability of photo chemical etching, inductively coupled with plasma and electron are used. Cyclotron resonance methods are used to etch 4H-SiC, but at very low rate (a maximum etch rate of 0.97 %m/ min (for inductively coupled reactor using ${\rm SF}_6$ based gaseous mixture).

D. Laser based Micromachining of SiC

Interaction of material with laser pulses generally involves the following fundamental processes that take place on different time scales after a pulse incidence(98). Firstly, light absorption takes place at a femtosecond timescale accompanied by ejection of excited electrons. Secondly, the excited electrons transfer their energy to the lattice in several tens of picoseconds through electron-phonon collision and induce melted zone in the bulk material. Following this, melted material evaporates on a nanosecond time scale and expands into the air in the form of atomic-sized particles in the ablation plume. Melt expulsion may also occur, termed hydrodynamic ablation, due to the recoil pressure of plume and result in large particles or clusters following the plume. The specific ablation mechanism in laser micromachining and the resulting morphology of the ablated region such as aspect ratio and sidewall roughness varies with material property, laser wavelength, power intensity as well as pulse duration. The process of multiphoton excitation of electrons and collisional ionization is reported to play vital roles in semiconductor and dielectric laser ablation with a sharp threshold of power intensity observed. This mechanism become dominant when ultra-short laser pulses are employed for ablation, whereby a much increased portion of laser energy is transferred to electrons compared to the portion further transferred to the lattice (99). Under high intensity picoseconds-pulsed laser fluence, the laser irradiated portion of n-type SiC material can be directly transformed into plasma on a few picoseconds time scale trough Coulomb explosion resulting in clean surfaces of the cleaves that outperform the results obtained with thermal ablation (100). The maximum amount of laser beam power to be converted into processing power, with no or little peripheral heating in order to keep the Heat Affected Zone (HAZ) as small as possible. The ability to focus the beam to a very small spot leads to an increase in effective process power. Laser based bulk micro aching for MEMS application has basically classified in to two based on the operation 1) Mask less bulk micro machining 2)laser assisted precise micro-machining through mask.

a. Maskless micromachining

Figure 4 shows the schematic layout of laser based maskless micro-machining. Conventional CO₂ pulsed laser has been used to produce micro holes of diameter ranging from 100 - 200 μ m and depth upto 400 v m (100). Silicon carbide sample surface were textured using micro holes having centre to centre distance of about 200 μ m hole dimension in the range of 80 – 100 μm was machined. Square diaphragms with a nominal size of $1.5 \, \text{mm} \times 1.5 \, \text{mm}$ were fabricated from bulk 6H-SiC wafers using a Q-switched Nd:YAG laser operating at a wavelength of 1064 nm. an average power of 0.35W, a pulse repetition rate of 3 kHz, and a pulse width of 100 ns(101). These parameters were chosen, based on previous experiments, to minimize surface roughness. Analysis of laser-machined diaphragms revealed that the average thickness of a diaphragm was 151 µ m which is composed of two layers. One is a soft, black layer with a thickness of about 83 μ m consisting of silicon, oxygen, and carbon. The other layer was a hard, virgin SiC layer with a thickness of 68 µ m (102). Nd:YAG laser at three different harmonics with pulse durations in the ps to ns regime of SiC with 1(omega), 2(omega) . 3(omega) -Nd:YAG laser radiation is performed in various processing gas atmospheres as a function of processing variables showing the influence of the heat and pressure load onto the precision of geometric structures generated (103). The physical and chemical processes involved in micromachining with laser radiation are characterized by a machine vision system and the produced structures are analyzed by profilometry, optical and electron microscopy as well as X- photoelectron spectroscopy (104). Laser ablation of silicon carbide with 193 and 248 nm excimer nano seoned laser and with 500 fs , 248 nm Femto second laser, it was observed that, experiments in UV shows a significant difference in the quality between 193 nm and 248 nm radiation. The cavity indicates a higher thermal impact in the case of 248 nm radiation. The application of 500 fs pulse at this particular wavelength results in an improved edge definition and surface quality of the ablated area(105).

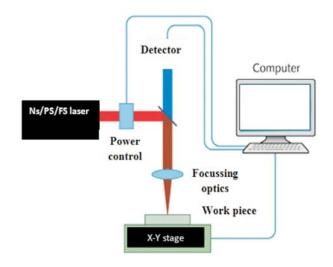


Fig. 4. Schematic layout of laser based maskless micro-machining.

b. Bulk micro machining through Mask

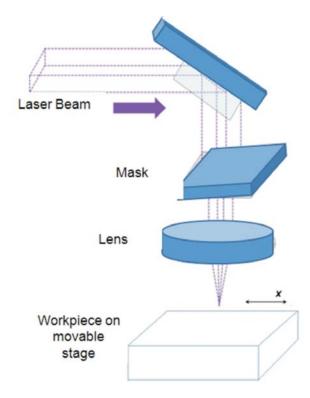


Fig. 5. Schematic layout of laser based micro-machining using mask.

Figure 5 shows the schematic layout of laser based micro-machining using mask. The mask can be replaced by a diamond tool and an IR diode laser is made to pass through a diamond tool for generating micro-scratches on 4H-SiC surface(106). Greyscale

masks have been successfully implemented in an excimer laser micromachining system to produce structures with a continuous profile (107). During this work, it was found possible to machine structures to depths of several tens of microns with no observable mask degradation. The greyscale mask transmissions were defined using a matrix of pixels whose dimension was smaller than the resolution limit of the optical system in the laser micromachining systemv(108). By reduction-projecting the greyscale mask pattern onto the workpiece, the local fluence at the workpiece could be predetermined and hence the local machining rates This enabled three-dimensional (3D) structures to be fabricated at the workpiece in a single machining operation(109). Under the experimental conditions used in this investigation, the rate at which material was ablated was found to depend linearly on the percentage transmisson of the mask(110).

E. SiC Surface Micromachining

Surface micromachining is a popular technology used for fabrication of MEMS devices (111-113). There are very large numbers of variations of how surface micro machining is performed depending on the material and etchant combinations that are used. However, the common theme involves a sequence of steps starting with the deposition of some thin film materials to act as a temporary mechanical layer onto which the actual device layers are built, followed by the deposition and patterning of thin film device layer of materials which is referred to as the structural layer; then followed by the removal of the temporary layer, thereby allowing the structural layer to move. Figure 6 merely explains the concept of surface micro machining

where, the oxide layer is temporary and is commonly referred to as sacrificial layer, subsequently; a thin film layer of SiC is deposited. Finally the sacrificial layer is removed and the silicon carbide layer is free to move as a cantilever.



Fig. 6. Laser based micromachining with mask

explained in the previous single-crystal SiC films must be grown directly on single-crystal silicon. Some of the reasons surface micromachining is so popular is that it provides for precise dimensional control in the vertical direction. This is due to the fact that the structural and a sacrificial layer thickness are defined by deposited film thickness whihe can be accurately controlled. Also, surface micro-maching provides for precise dimensional control in the horizontal direction, since the structural layer tolerance is defined by the fidelity of the photolithography and etches processes are used. Other benefits of surface micro machining are that a large variety of structure, sacrificial and etchant combinations can be used; which are compatible for MEMS device.

Fluorine-based gas mixtures have been shown to be the most effective gas for SiC etching in terms of high etch rate (114). The F species in the plasma can

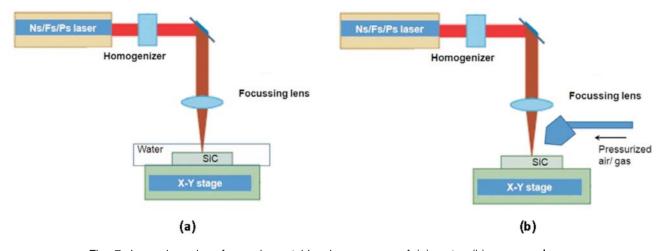


Fig. 7. Laser based surface micro etching in presence of (a) water (b) gaseous layer

react with both Si and C to form volatile compounds as reaction products. Commonly, SF₆ and O₂ gas mixtures are used because the optimum amount of O₂ addition provides another pathway for volatilising C in the forms of CO, CO₂ (115-117) and there by increases etch rates. SiC etch rate of 1.05 im/min(118-119) has been achieved in an inductively coupled plasma reactor using SF₆ and O₂ gas mixtures. However, obtainable SiC etch rates strongly depend on the plasma conditions used such as pressure, flow rate, chuck power, etc. It was investigated (120-126) that this technique will induces little surface and the etch rates are much lower, laser based surface micromachining can act as an alternative approach, since the focussing point can be easily adjusted within the sacrificial layer, the etching can be performed in the presence of gas and water environment to remove the debris without any defect in the surface. Another mechanism, which is non thermal and referred to as photo-ablation, occurs when organic materials are exposed to ultraviolet radiation generated from excimer, harmonics YAG or other UV source.

F. Laser Based Surface Micromachining of SiC

In contrast to the chemical-based micro-fabrication methods, water assisted laser ablation of SiC is capable of higher etching rates (126-129) and precise control of via size with advancement of reducing in the number of processing steps as masking, machining independent of crystal structure, and curved surfaces. Laser etching in water, with regard to laser pulse parameters, is similar to the Laser Shock Processing process as shown in figure 7(a), with the exception that shock is not desired, while ablation is desired. This is achieved by lowering somewhat the fluence and increasing the pulse number. The fluence is taken at the level where the material at least melts, but usually vaporizes and ionizes (plasma forms).

Recently, there have been increasing studies on liquid-assisted laser micromachining, in particular on the processing parameters. It was found that liquid-assisted laser micromachining can reduce the extent of HAZ, micro-cracks, spatter re-deposition and tapering of drilling (130-132). The etching rate on liquid-assisted laser drilling strongly depends on liquid film thickness(133-135). The reduction of the extent of HAZ, micro-cracks, and spatter redeposition can explain by the cooling effect liquid. In addition, using pure water, the background etching of non-illuminated areas was

eliminated. The main reason for water-assisted laser etching is the elimination of debris re-deposition in the machined area. This results in cleaner and more precise surface profiles and avoids any need for work piece after cleaning, which is usually done in an ultrasound bath(136). In some case the usage of water may result in formation of oxide layer, in those condition a thin layer of gas or air can act as in a medium in surface etching as showed in figure 7(b).

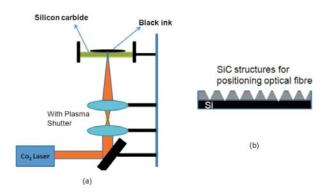


Fig. 8. (a) Schematic layout of LIBWE (b) proposed formation of V-groove on SiC

There are reports in non-thermal, laser assisted photo-electrochemical etching technique for n-type $\beta\text{-SiC}$ etch rates as high as 100 μ m/min. UV radiation is necessary for efficient photo-generation of holes near the surface. These holes are transported in the presence of an external bias to the semiconductor/liquid interface, where dissolution occurs through the aniodic oxidation of the SiC and the removal of oxide present in the electrolyte (137).

Laser Induced Back side wet etching is a non thermal micro-etching in which the etchant plays a major role on surface etching, however interaction of laser have their own effect in etching. figure 8(a) shows the working principle of LIBWE in which a liquid layer is kept in contact with the silicon carbide and the silicon carbide act as a transparent medium when the Co₂ laser is made to pass thorough the surface, the laser focused on the absorbed liquid medium resulting in formation of laser induced plasmas. Confining these plasma may lead to etch silicon from the rear side. Figure 8(b) shows the proposed V-groove structure for MEMS application. Figure 9 shows the SEM micrograph of the silicon wafer, where a through hole is micro machined using deionised water as an etchant.

Detail study of this LIBWE process will lead to precise micro etching of high quality SiC surface.

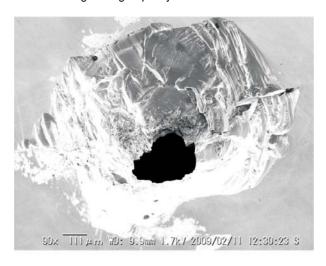


Fig. 9. Through hole developed on Si through LIBWE

IV. SUMMARY

The materials properties and the state of SiC technology was reviewed with respect to MEMS device and systems. The materials property offers existing possibilities to extend the applicability of sensor principle. The excellent stability of SiC enables operation in harsh environment such as high temperature, high pressure and chemical corrosive media. The method of micro-fabrication of SiC basically governs the reliability and effectiveness of SiC for MEMS.

Wide varieties of approaches are available for micro-fabrication and micro-etching of SiC. The conventional deposition of SiC includes CVD, PECVD etc. However the conventional systems have their own limitations towards micro-fabrication. These limitations can be overcome by laser based micro-fabrication. The conventional PLD technique can be appropriately tailored for different MEMS application, which includes 1) large area thin film 2) selective deposition 3) nanostructure development.

In addition to micro-deposition, micro-etching plays a major role for fabricating MEMS device. The conventional chemical based etching leads to defect in crystal structure. In contrast to the chemical-based micro-fabrication methods, water assisted laser ablation of SiC is capable of higher etching rates and precise control of via size with advancement of reducing in the

number of processing steps as masking, machining independent of crystal structure.

The development of efficient laser based micro-fabrication and micro-etching technique will lead to efficient engineering of SiC for a remarkable development of MEMS device.

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